

Fig. 1

Prior Art

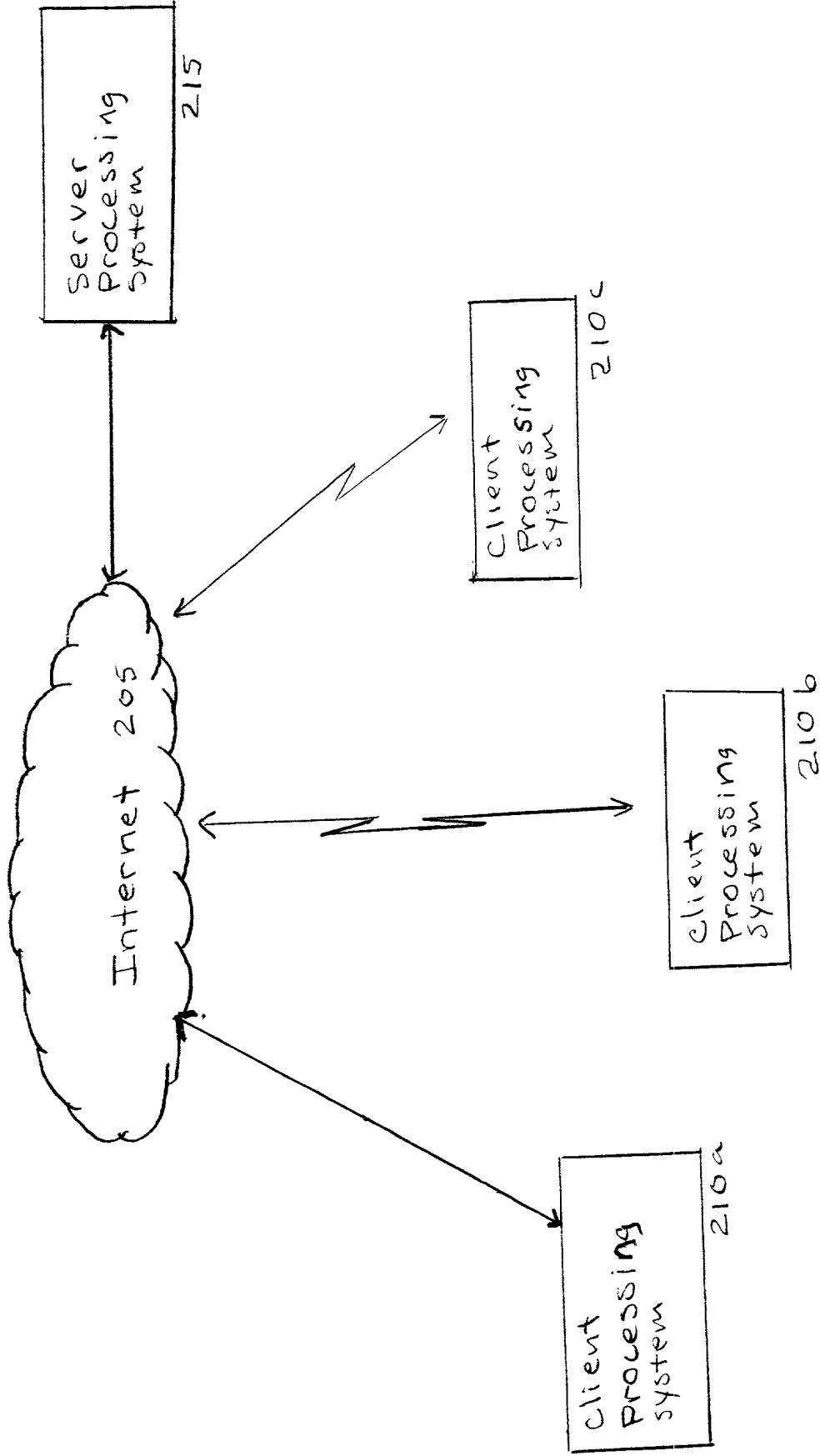
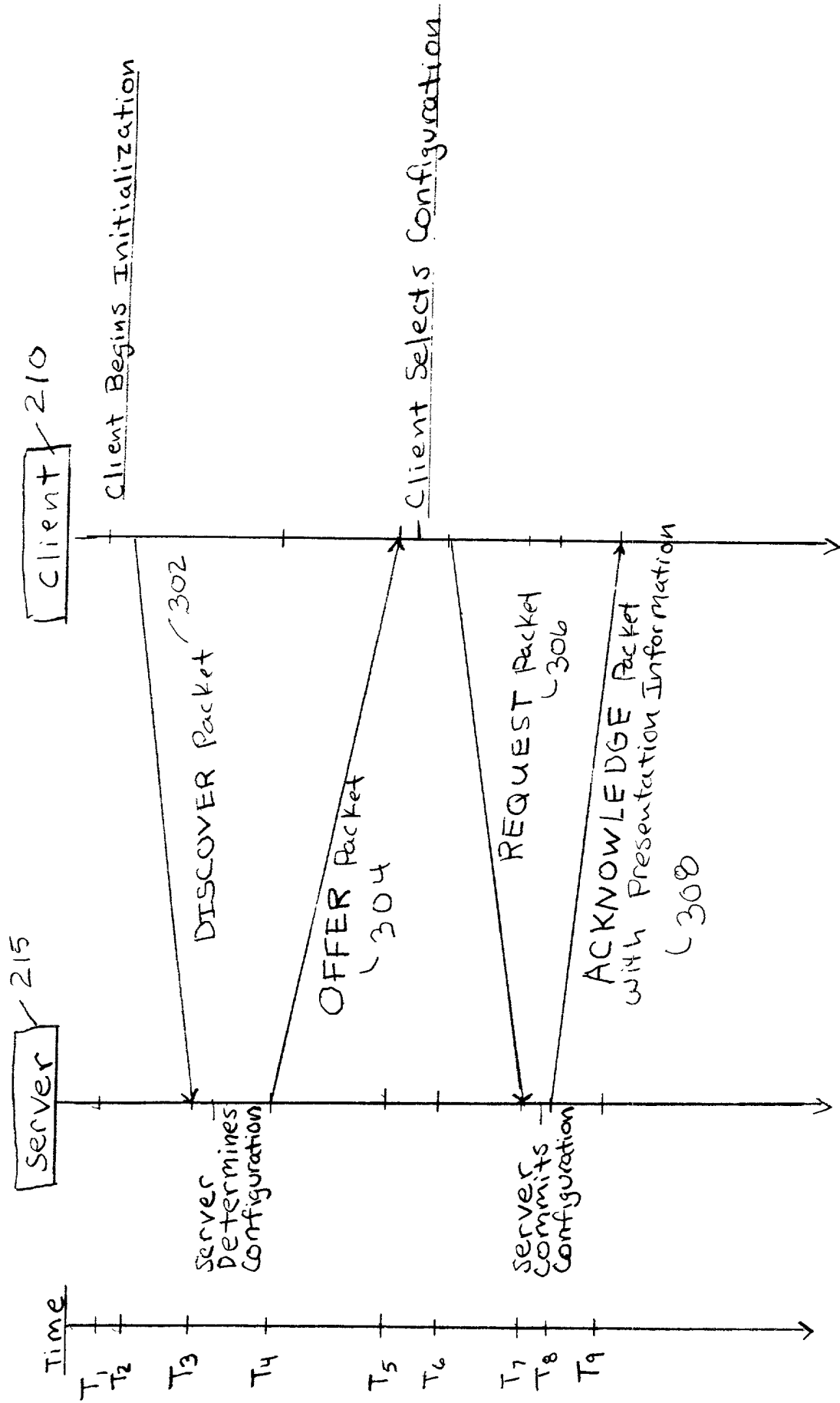


Fig. 2



Message Flow Time Line

Fig 3

FOR "99999999"

400

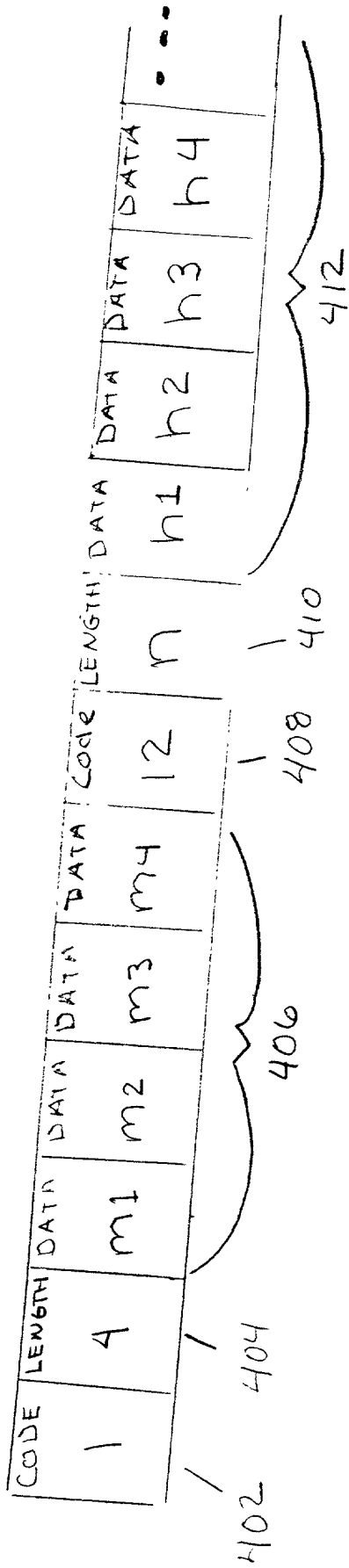


Fig. 4A

DHCP Packet Format

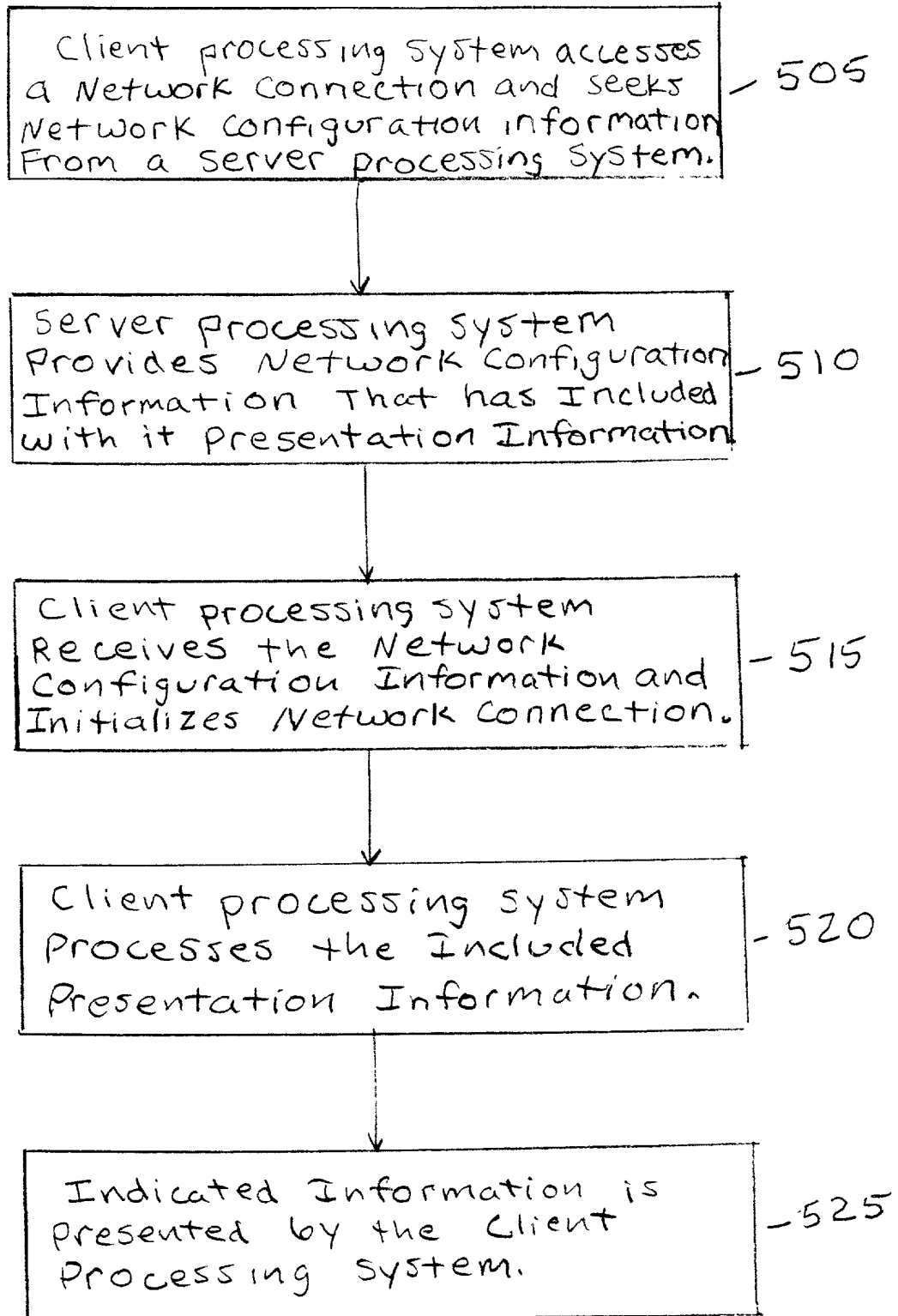


Fig. 5

Digital Processing System (DPS) Block Diagram

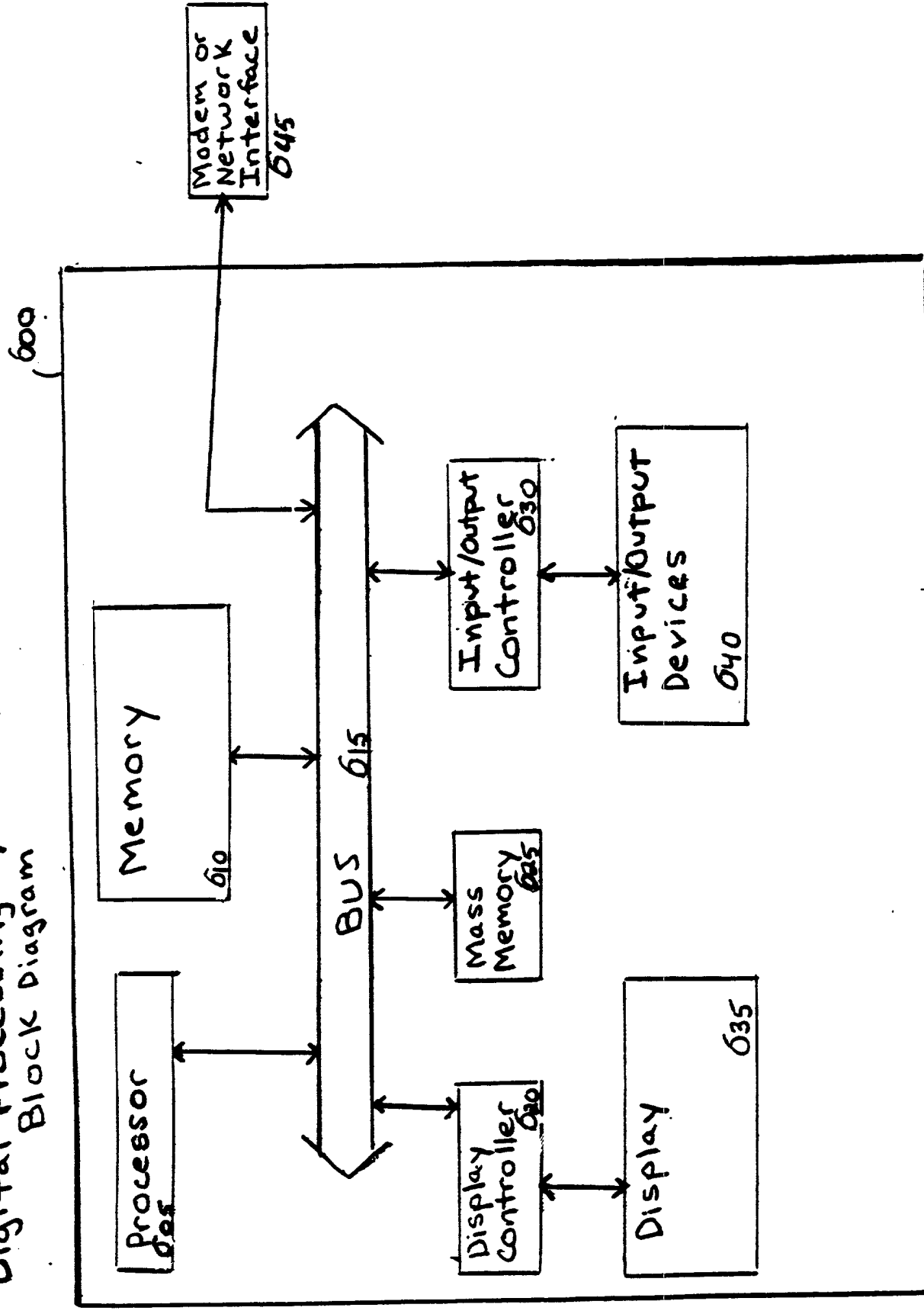


Fig. 6